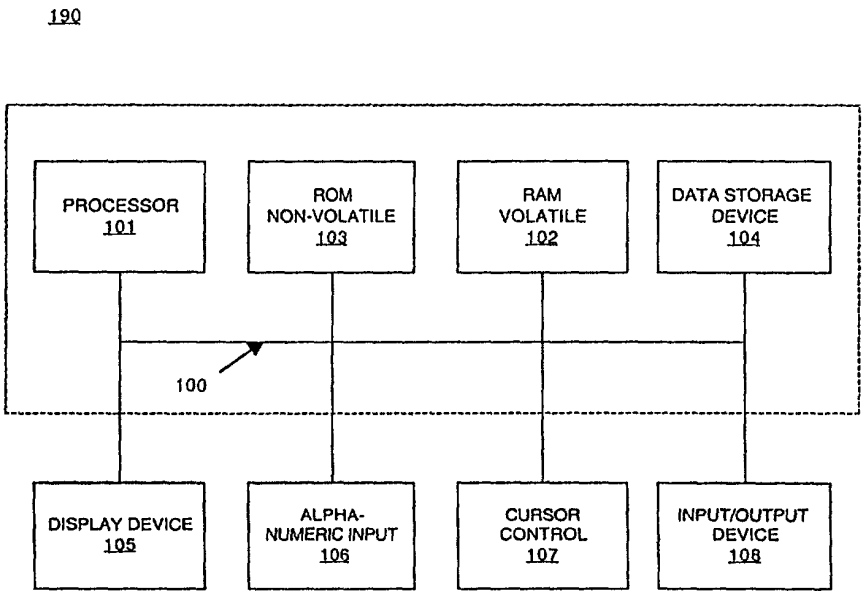
190
PROCESSOR
101
ROM
NON-VOLATILE
103
RAM
VOLATILE
102
DATA STORAGE
DEVICE
104
100
DISPLAY DEVICE
105
ALPHA-
NUMERIC INPUT
106
CURSOR
CONTROL
107
INPUT/OUTPUT
DEVICE
108

Figure 1

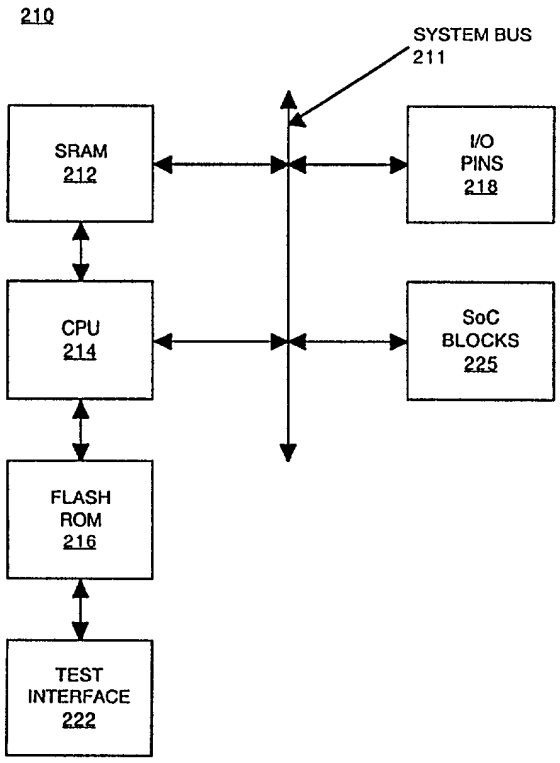
210
SYSTEM BUS
211
SRAM
212
I/O
PINS
218
CPU
214
SoC
BLOCKS
225
FLASH
ROM
216
TEST
INTERFACE
222

Figure 2A

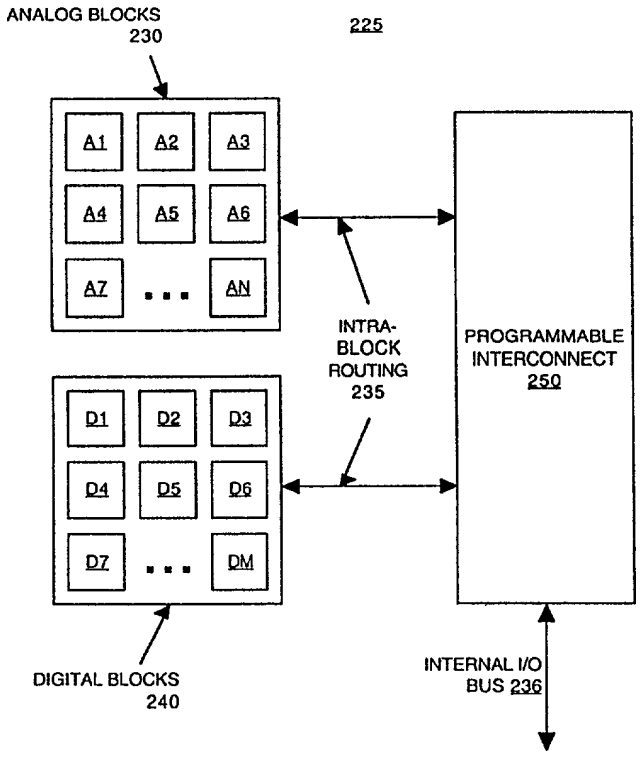
ANALOG BLOCKS
230
225
A1
A2
A3
A4
A5
A6
A7
AN
INTRA-
BLOCK
ROUTING
235
PROGRAMMABLE
INTERCONNECT
250
D1
D2
D3
D4
D5
D6
D7
DM
DIGITAL BLOCKS
240
INTERNAL I/O
BUS 236

Figure 2B

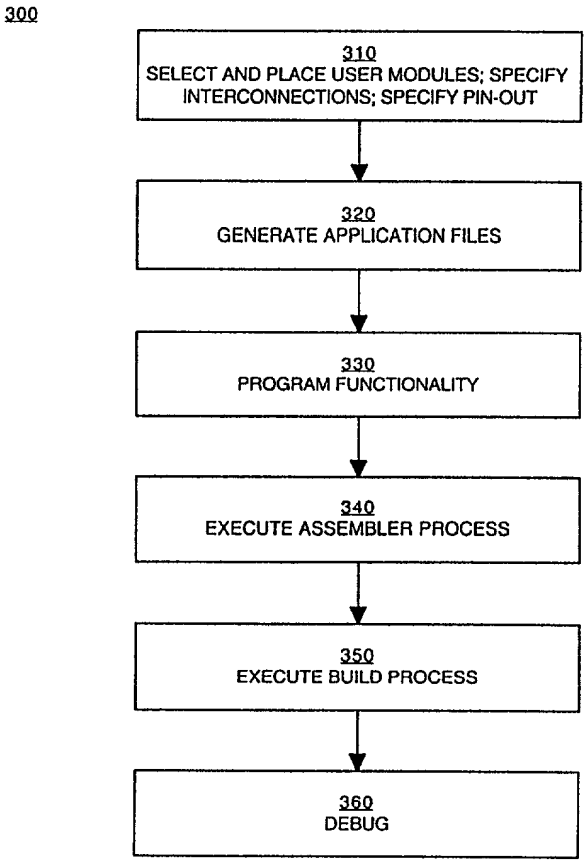
300
310
SELECT AND PLACE USER MODULES; SPECIFY INTERCONNECTIONS; SPECIFY PIN-OUT
320
GENERATE APPLICATION FILES
330
PROGRAM FUNCTIONALITY
340
EXECUTE ASSEMBLER PROCESS
350
EXECUTE BUILD PROCESS
360
DEBUG

Figure 3

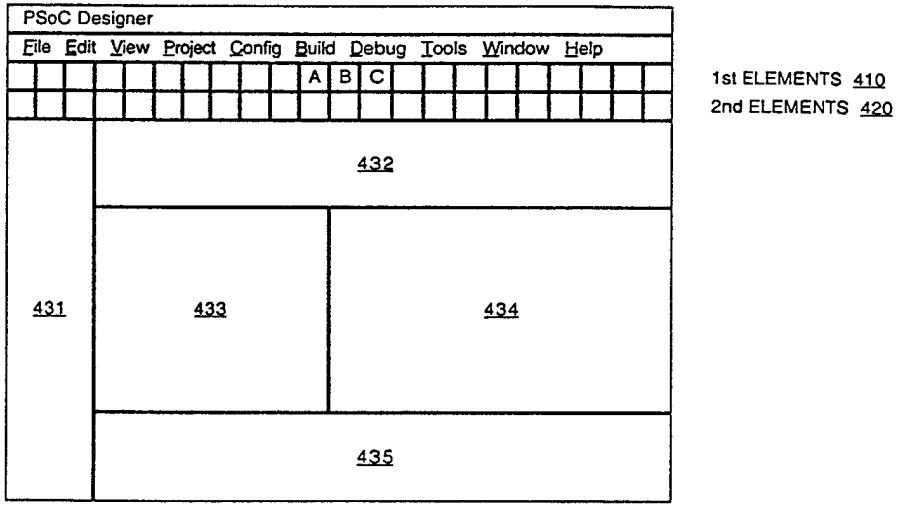
400
PSoC Designer
File Edit View Project Config Build Debug Tools Window Help
A
B
C
1st ELEMENTS 410
2nd ELEMENTS 420
432
431
433
434
435

Figure 4A

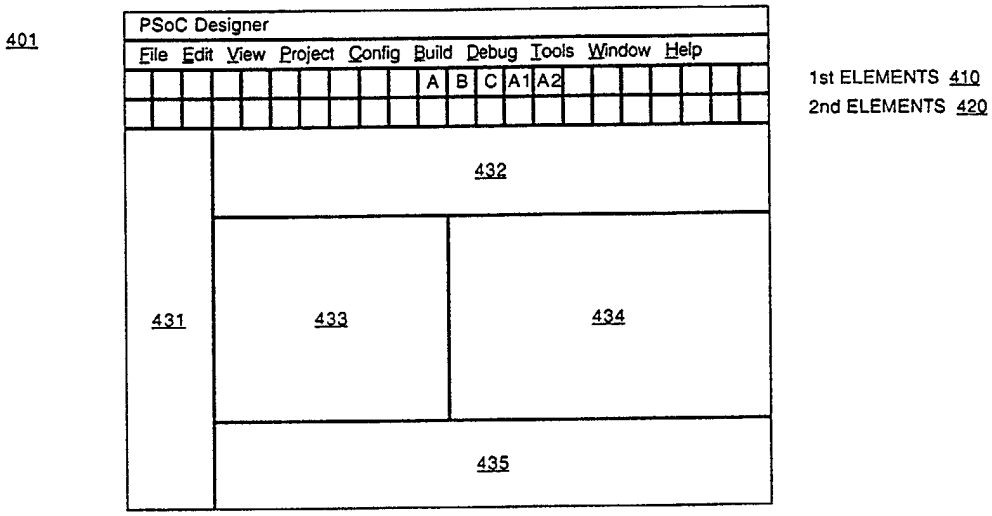
401
PSoC Designer
File Edit View Project Config Build Debug Tools Window Help
A B C A1 A2
1st ELEMENTS 410
2nd ELEMENTS 420
432
431
433
434
435

Figure 4B

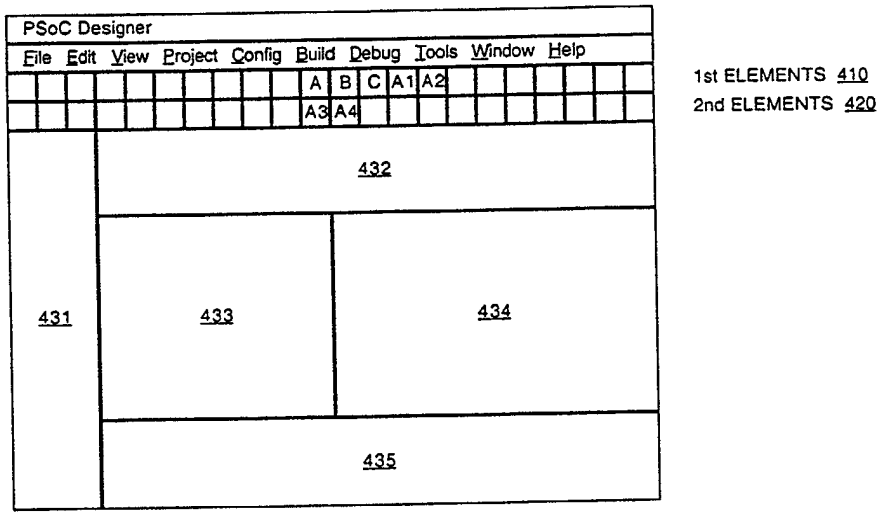
402
PSoC Designer
File Edit View Project Config Build Debug Tools Window Help
A B C A1 A2
A3 A4
1st ELEMENTS 410
2nd ELEMENTS 420
431
432
433
434
435

Figure 4C

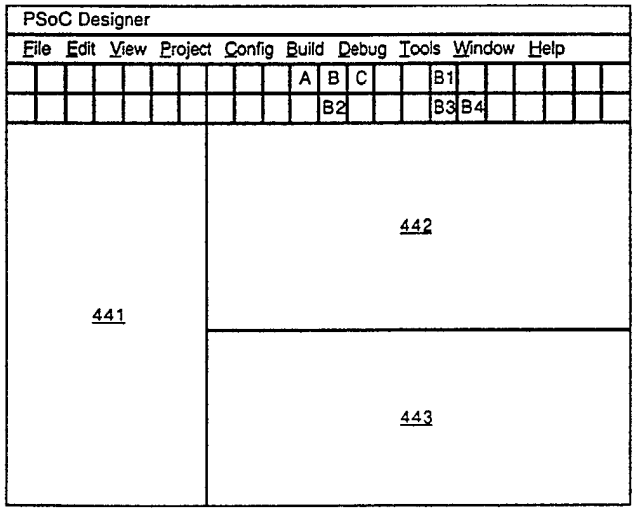
403
PSoC Designer
File Edit View Project Config Build Debug Tools Window Help
A B C B1
B2 B3 B4
1st ELEMENTS 410
2nd ELEMENTS 420
441
442
443

Figure 4D

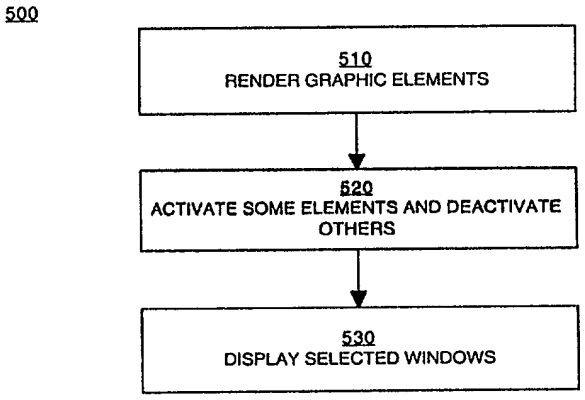
500
510
RENDER GRAPHIC ELEMENTS
520
ACTIVATE SOME ELEMENTS AND DEACTIVATE OTHERS
530
DISPLAY SELECTED WINDOWS

Figure 5

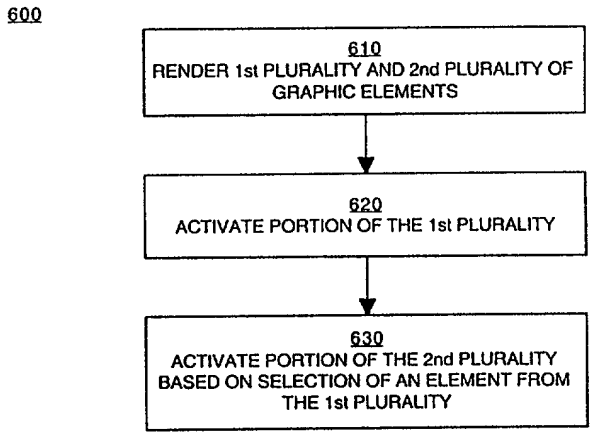
600
610
RENDER 1st PLURALITY AND 2nd PLURALITY OF GRAPHIC ELEMENTS
620
ACTIVATE PORTION OF THE 1st PLURALITY
630
ACTIVATE PORTION OF THE 2nd PLURALITY BASED ON SELECTION OF AN ELEMENT FROM THE 1st PLURALITY

Figure 6

700

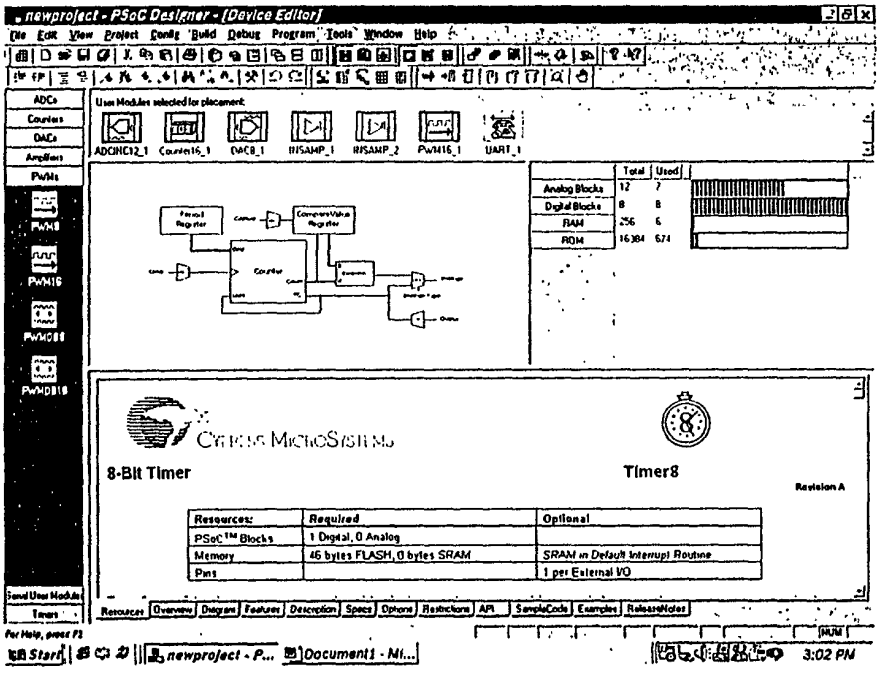
newproject - PSoC Designer - [Device Editor]
File Edit View Project Config Build Debug Program Tools Window Help
ADCs
Counters
DACs
Amplifiers
PWMs
User Modules selected for placement:
ADCINC12_1
Counter16_1
DAC8_1
RISAMP_1
RISAMP_2
PWM16_1
UART_1
Total
Used
Analog Blocks
Digital Blocks
RAM
ROM
CYPRESS MICROSYSTEMS
8-Bit Timer
Timer8
Revision A
Resources
Required
Optional
PSoC™ Blocks
1 Digital, 0 Analog
Memory
46 bytes FLASH, 0 bytes SRAM
SRAM in Default Interrupt Routine
Pins
1 per External I/O
Resources
Overview
Diagram
Features
Description
Specs
Options
Restrictions
API
SampleCode
Examples
ReleaseNotes
Start
newproject - P...
Document1 - Mi...
3:02 PM

Figure 7